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Facsimile Number: 703-746-4060

Transmission Date: August 9, 2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kowalski, et al.

Docket No.: INF-125

Serial No: 10/748,332

Art Unit: 2811

Date Filed: December 31, 2003

TITLE: Device Architecture and Process for Improved Vertical Memory Arrays

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Legal Assistant

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kowalski, et al. Attorney Docket No.: INF-125  
Application No.: 10/748,332 Examiner: TBD  
Filed: December 31, 2003 Group Art Unit: 2811  
Title: Device Architecture and Process for Improved Vertical Memory Arrays

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Associate Power of Attorney

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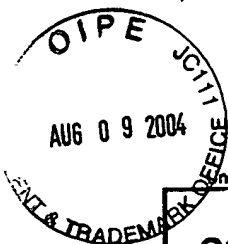
I hereby appoint as associate attorneys of record all attorneys at customer number 25962.

Also, please direct all future correspondence to:

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Respectfully submitted,

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PTO/SB/122 (08-03)

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Application Number	10/748,332
Filing Date	12/31/2003
First Named Inventor	Kowalski, et al.
Art Unit	2811
Examiner Name	TBD
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Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/06).
- ☒ Attorney or Agent of record. Registration number 35,272
- ☐ Registered practitioner named in the application transmittal letter in an application without an Executed oath or declaration. See 37 CFR 1.33(a)(1). Registration Number \_\_\_\_\_

Typed or Printed Name Ira S. Matsil

Signature 

Date August 6, 2004

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NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple Forms if more than one signature is required, see below.

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